

FIG 1A

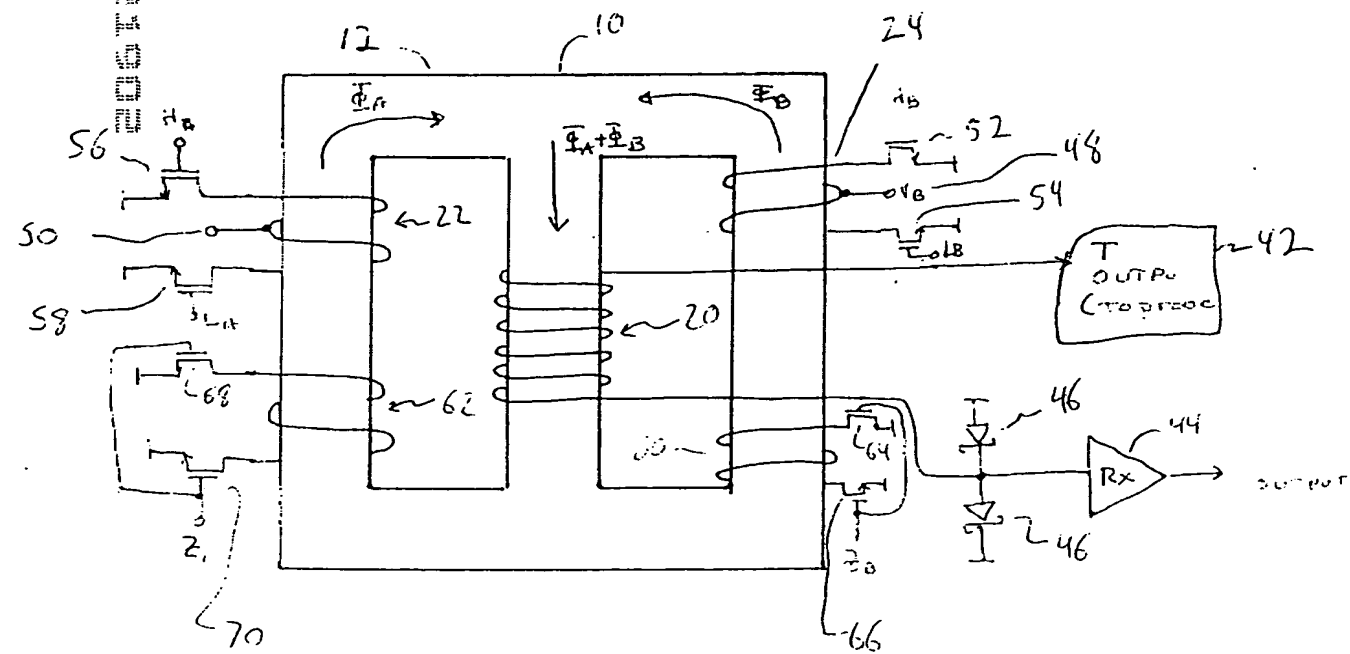


Figure 3

10073690.024902

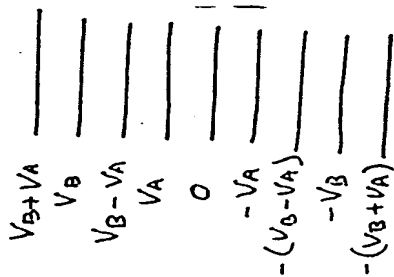


FIG 1B

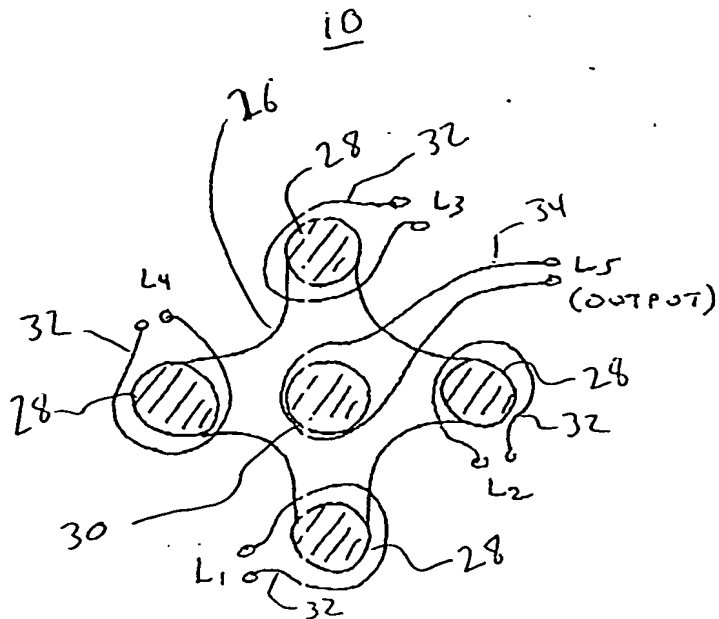


FIG. 2

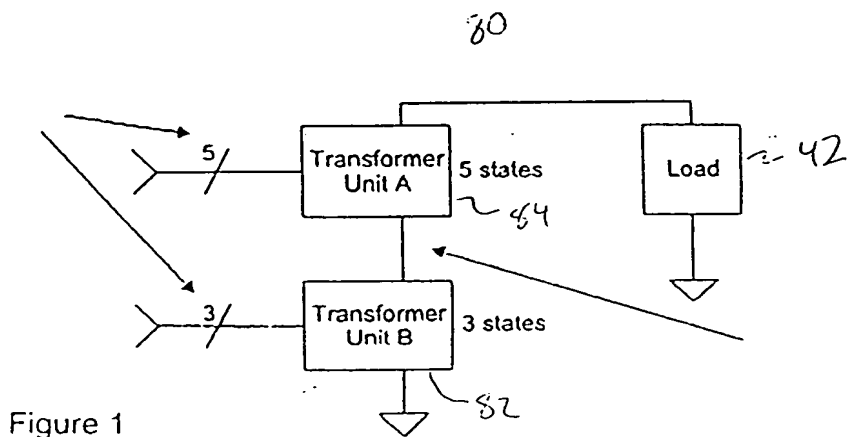


Figure 1

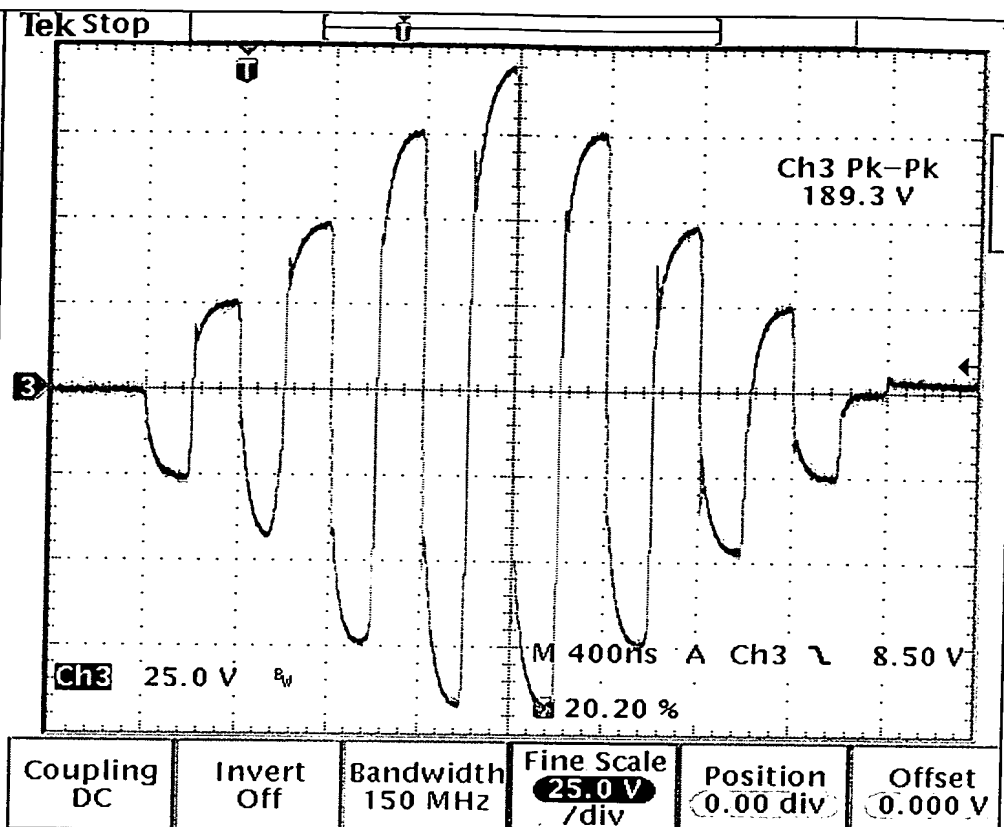
FIG. 6

9-Level TX  
(50  $\Omega$  Load)

F=2.5 MHz

$V_b=15V$   
 $V_a=5V$

Figure 5



H2

L2

Z2

H1

L1

Z1

Control Logic

Figure 4

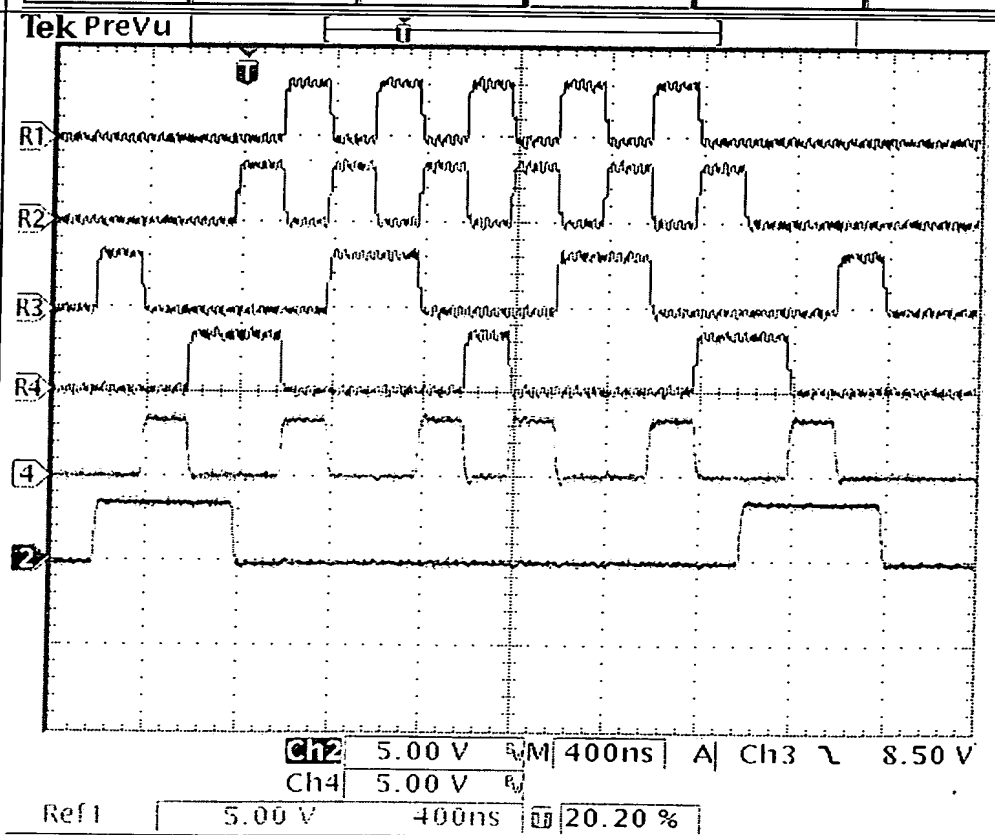




FIG. 7